Selected Curriculum for
Interest in VLSI
Advising Sheet

Computer Engineering master’s students are required to take one course from each of the four groups listed below.

Group “A”
☐ CSCE 5510 Wireless Communications
☐ CSCE 5520 Wireless Networks and Protocols
☐ CSCE 5580 Computer Networks

Group “B”
☐ CSCE 5160 Parallel Processing and Algorithms
☐ CSCE 5610 Computer System Architecture
☐ CSCE 5640 Operating System Design

Group “C”
☐ CSCE 5440 Real-Time Software Development
☐ CSCE 5612 (5933) Embedded Hardware & Software Design
☐ CSCE 5620 Real-Time Operating Systems

Group “D”
☐ CSCE 5730 Digital CMOS VLSI Design
☐ CSCE 5740 Topics in Modern Electronic System Design
☐ CSCE 5760 Design for Fault Tolerance

Suggested Courses:

☐ CSCE 5510 Wireless Communications 3 sch
☐ CSCE 5520 Wireless Networks and Protocols 3 sch
☐ CSCE 5540 Introduction to Sensor Networks 3 sch
☐ CSCE 5580 Computer Networks 3 sch
☐ CSCE 5610 Computer System Architecture 3 sch
☐ CSCE 5612 (5933) Embedded Hardware and Software Design 3 sch
☐ CSCE 5615 (5933) Networks-on-Chip 3 sch
☐ CSCE 5620 Real-Time Operating Systems 3 sch
☐ CSCE 5640 Operating System Design 3 sch
☐ CSCE 5730 Digital CMOS VLSI Design 3 sch
☐ CSCE 5740 Topics in Modern Electronic System Design 3 sch
☐ CSCE 5760 Design for Fault Tolerance 3 sch
☐ CSCE 6731 Advanced Topics in VLSI Systems 3 sch

Major Professors Comments/Suggestions:
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- For MS with thesis, the total number of hours required is 30.
- For MS without thesis, the total number of hours required is 36.
- To continue in good standing, a student must maintain a 3.0 GPA overall.
- Maximum of one CSCE 5934 Directed Study and/or CSCE 5900-5930 Special Problems course is permitted and the CSCE 5932 Internship course may not be included on the degree plan.